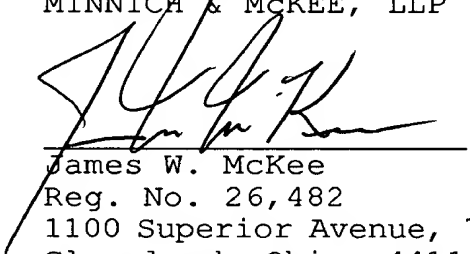


REMARKS

Claims 3-7 and 27 as presented in Form (PCT/IPEA/409) have been canceled, claims 23 and 24 as presented in Form (PCT/IPEA/409) have been amended, and claims 35 and 36 have been added. Accordingly, claims 1, 2, 8-26, and 28-36 are presented for examination. Claims 23 and 24 were amended and claims 35 and 36 were added for the sole purpose of removing multiple claim dependencies, thus eliminating the filing fee surcharge that would otherwise have been necessary had the claims not been amended.

Entry of the foregoing amendments is respectfully requested.

Respectfully submitted,
FAY, SHARPE, FAGAN,
MINNICH & MCKEE, LLP



James W. McKee
Reg. No. 26,482
1100 Superior Avenue, 7th Floor
Cleveland, Ohio 44114-2518
(216) 861-5582

CERTIFICATE OF MAILING

I hereby certify that this PRELIMINARY AMENDMENT is being deposited with the United States Postal Service via EXPRESS MAIL in an envelope numbered EL852784359US and addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231, on May 2, 2001.



Georgeen B. George

Encl.: Version with Markings to Show Changes Made

VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Specification:

On page 8, the paragraph beginning on line 15 and ending on line 25 should be amended as follows:

Alternatives to the embodiments shown in Figures 2A and 2B are possible. For example, in Figure 2A the pixels are located to the right of pixels 1A and the number of source lines per [pwr] pixel is reduced. However, this can be easily rearranged so that the pixels 1B are located beneath the pixels 1A as shown in Figure 2B, and reconfigured to increase the fill factor while still reducing the number of source lines per pixel [so that the number of gate lines per pixel is reduced instead]. Likewise in Figure 3B, the pixels 1C are shown disposed to the right of the pixels 1A, and with dimensions selected to increase fill factor [thereby reducing the number of source lines per pixel, rather than the number of gate lines per pixel as in Figure 3A].

Please amend claims 23 and 24 that are presented in the ANNEXES to the International Preliminary Examination Report Form (PCT/IPEA/409) as follows:

23. (Amended) The improvement of claim [2,] 11, [or 18,] wherein each said thin-film-transistor (TFT) is a single gate device.

24. (Amended) The improvement of claim [2,] 11, [or 18,] wherein each said thin-film-transistor (TFT) is a dual gate device.